

VLSI for DSP

Homework 3

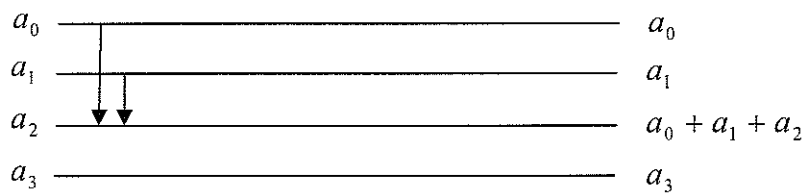
資工碩一 79883107 蔡維峻

(a) Design Prefix Sum.

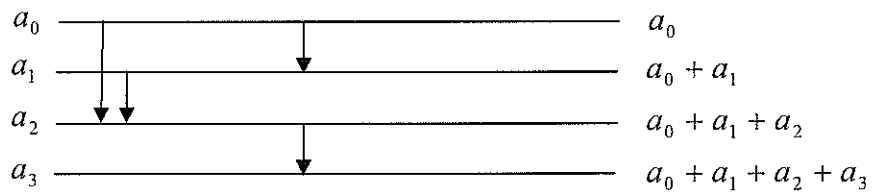
B

Ans:

Step1.



Step2.



VLSI for DSP

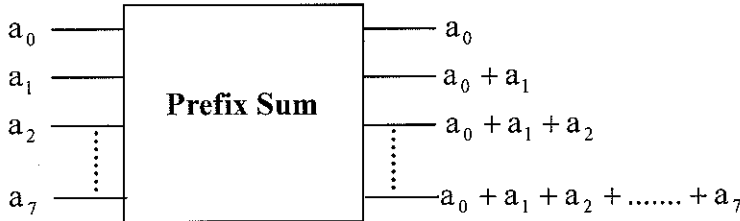
Homework No.3

79882304 黃郁書

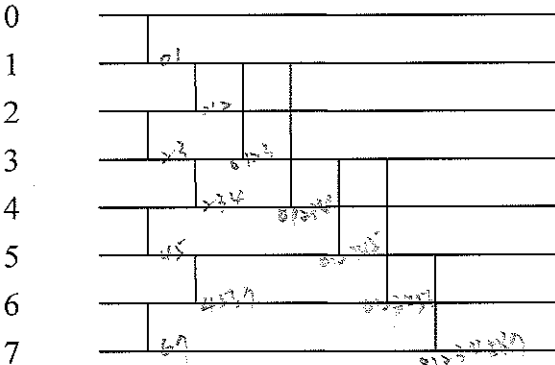
You can choose one of them or answers all the questions.

(a) Design Prefix Sum.

Assume Prefix Sum $a_0 \sim a_7$ and maximum fan-in is 3.



$a_0 \sim a_7$ to simplify like 0~7



BT

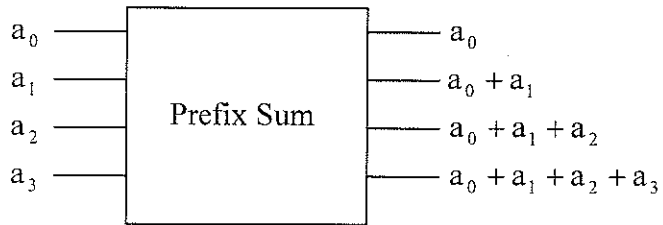
(b) How to sorting the numbers in time $O(1)$. Hint: 3D mesh.

Homework No.3

康弘 弘

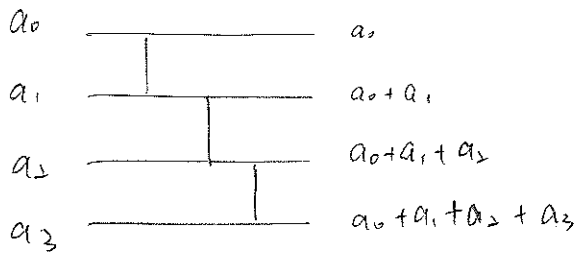
You can choose one of them or answers all the questions.

(a) Design Prefix Sum.

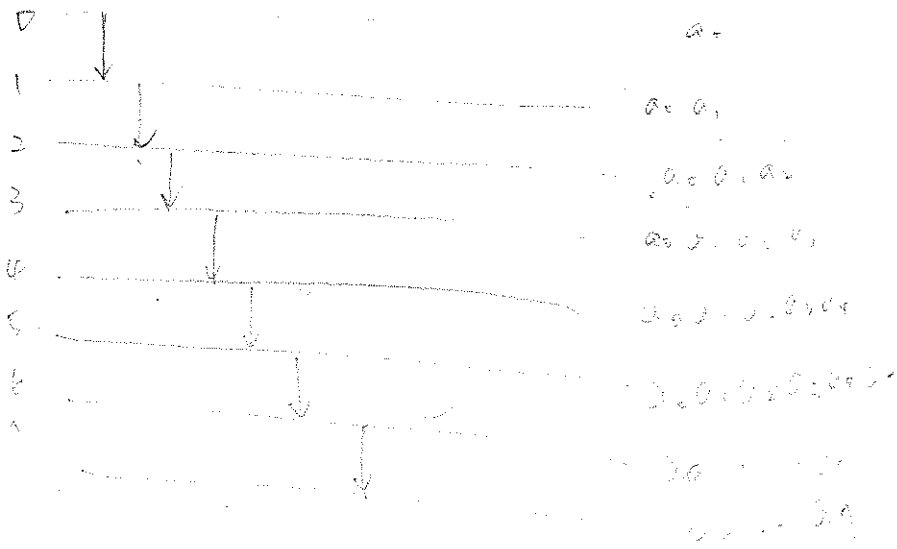


C

(b) How to sorting the numbers in time $O(1)$. Hint: 3D mesh.



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